

J1050 U.S. PTO
10/083255
02/27/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083255	FILING DATE 02/27/2002	CLASS 324	SUBCLASS	GAU 2858	EXAMINER 2839
**APPLICANTS: Udo Ryujiro; Arai Masatsugu; Edamura Manabu;					
**CONTINUING DATA VERIFIED: <i>none</i> <i>MDH</i> <i>6/4/02</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>none</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		500.41296X00			
Verified and Acknowledged Examiners's initials <i>MDH</i>					
TITLE : Semiconductor processing apparatus and wafer sensor module					

U.S. DEPT. OF COMM. PAT. & TM. PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Total Claims	
		Print Claim for O.G.	
		Sheets Drwg. Figs. Drwg. Print Fig.	
		Primary Examiner	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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